



UNITED STATES PATENT AND TRADEMARK OFFICE

W
UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|--|-------------|----------------------|-------------------------|------------------|
| 08/999,663 | 12/18/1997 | EVAN GEORGE COLGAN | YO994-065XX | 4175 |
| 7590 | 05/19/2004 | | EXAMINER | |
| DANIEL P MORRIS IBM CORPORATION INTELLECTUAL PROPERTY LAW DEPT PO BOX 218 YORKTOWN HEIGHTS, NY 10598 | | | PARKER, KENNETH | |
| | | | ART UNIT | PAPER NUMBER |
| | | | 2871 | |
| | | | DATE MAILED: 05/19/2004 | |

Please find below and/or attached an Office communication concerning this application or proceeding.



UNITED STATES DEPARTMENT OF COMMERCE

U.S. Patent and Trademark Office

Address: COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

08999663

| APPLICATION NO./ CONTROL NO. | FILING DATE | FIRST NAMED INVENTOR / PATENT IN REEXAMINATION | ATTORNEY DOCKET NO. |
|---------------------------------|-------------|---|---------------------|
|---------------------------------|-------------|---|---------------------|

EXAMINER

ART UNIT

PAPER

05162004

DATE MAILED:

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner for Patents

The reply filed on 10/27/2003 is not fully responsive to the prior Office Action because of the following omission(s) or matter(s):

In the communication of page 4/23/2003, on the first page of that office action, applicant was instructed to show where each of the claimed limitations was supported in the specification of a list of claims including 77, 57-63, 67-70, and 71. Applicant only made a listing of the elements showing specification support of claim 77. It is necessary that applicant provide a complete list for each claim showing how each of the limitations is supported by the specification- which means listing out each claim and showing where each of the limitations is shown in the specification. For limitations such as the light blocking layer being non-conductive, it is necessary to show what layer it is applicant is referring to, but also where it indicates that the layer is non-conducting. For elements such as the transistor which applicant did at least partly address, if other layers are to be somehow construed as supporting the element. See 37 CFR 1.111.

Since the above-mentioned reply appears to be bona fide, applicant is given ONE (1) MONTH or THIRTY (30) DAYS from the mailing date of this notice, whichever is longer, within which to supply the omission or correction in order to avoid abandonment. EXTENSIONS OF THIS TIME PERIOD MAY BE GRANTED UNDER 37 CFR 1.136(a).

The reply filed on 10/27/2003 is not fully responsive to the prior Office Action because of the following omission(s) or matter(s): SEE . See 37 CFR 1.111.



Kenneth A. Parker
Primary Examiner
Art Unit: 2871